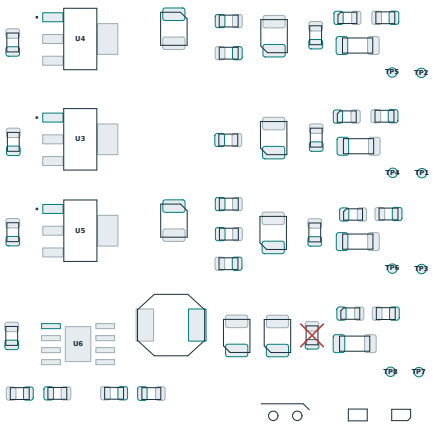
Power & signal identification

Regulated-rail probe points, indicators and drawing colours

REGULATOR / PROBE LOCATOR



Top view / 1.25:1

RAILS & INPUT

Nominal rail	Regulator	LED	Probe + / GND
+1.25V	U3	D3	TP1 / TP4
+2.5V	U4	D4	TP2 / TP5
+3.3V	U5	D2	TP3 / TP6
+5VREG	U6	D6	TP7 / TP8
12V-Supply	Input	D1	TP10 / TP9

All probe pairs are on top. TP10 (+12 V) / TP9 (GND) are beside the barrel input. Rail names are nominal values, not acceptance tolerances.

D14: ATX standby. U7 / 5VSB: ATX control. Mini-USB VBUS is unconnected. J10 supplies +5VREG / GND despite its saved Spare I/O value.

BUS COLOUR KEY

- Power rails
- Ground / return
- MC68000 interface
- Shared SRAM bus
- FPGA / ARM control
- Video / sync
- Stereo audio
- Joystick / PS/2 / serial

Drawing colours are reading aids. The saved project has one Default netclass with transparent colour fields. Named nets and pin connectivity govern.

Input: 12 V via J2 ATX or J8 barrel. Simultaneous-source operation is not specified.

Dennis, Ranko, Renee / Minimig.ca / Precision-Arrays

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Units: mm. Saved CAD governs. 3D views are illustrative.

A4 landscape. Print at 100% for the stated drawing scales.