Top assembly

Placement plan, fitted-side orientation and native 3D reference

ASSEMBLY NOTES

01 View from outside the top. Orient using the board outline and connector edges.

02 Reference plan: 1:2 at A4 / 100%. The 170 mm board is drawn 85 mm wide.

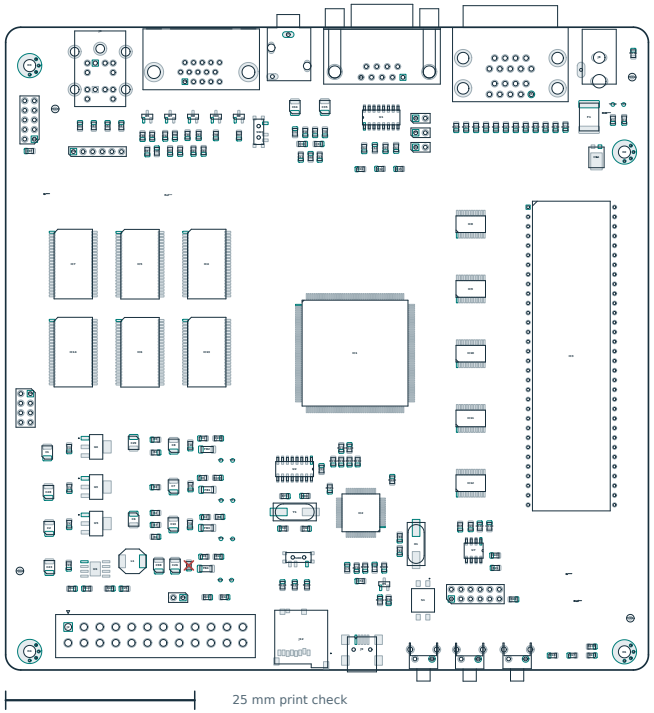
03 Teal marks pad 1 / A1. Check polarity and pin numbering against the footprint.

04 Red crosses mark saved DNP values. There are 18 options; native DNP flags are unset.

05 IC1: Spartan-3. IC2: ARM controller. IC3: MC68000. IC8-12: bus translation.

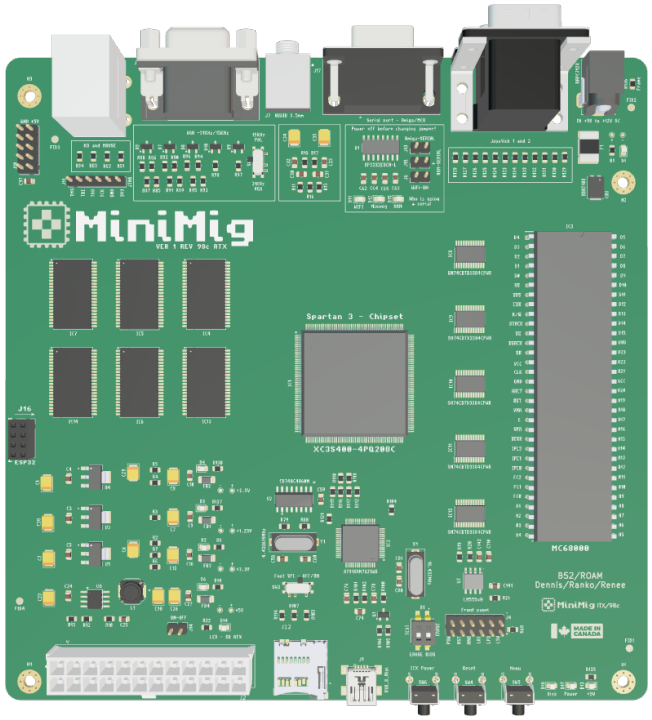
06 IC4-7 / IC13-14: six SRAMs, 6 MiB total. Shared 16-bit data and 19 address signals.

REFERENCE PLAN / 1:2



25 mm print check

3D ASSEMBLY VIEW



Side	Qty	SMD	DNP
Top	217	172	1
Bottom	175	175	17

Ref	Role	Device
IC1	FPGA / PQ208	XC3S400-4PQ208C
IC2	Controller	AT91SAM7S256D
IC3	CPU / DIP64	MC68000

Dennis, Ranko, Renee / Minimig.ca / Precision-Arrays

Revision 1.98itx | 2026-10-17 | PCB 2d3f20eb0dd8

Units: mm. Saved CAD governs. 3D views are illustrative.

A4 landscape. Print at 100% for the stated drawing scales.

Minimig 1.98itx / Top assembly

A4 · 2026-10-17 · Drawing 1/5